SPLVDS104RH

1:4 LVDS Fanout Buffer with Extended Common Mode

ORDERING INFORMATION

ORDER NUMBER	DESCRIPTION
SPLVDS104RH-EM	Engineering Model (EM)
SPLVDS104RH-QM	Qualification Model (QM)
SPLVDS104RH-FM	Flight Model (FM)

QUALITY LEVELS

SYMBOL	EM	QM	FM [1]
Wafer selection			■
WLAT including SEM, TID and lifetime tests			■
Ceramic dual in-line FP-16 package	■	■	■
Internal visual inspection according to ESCC 2049000		■	■
Hermetically sealed		■	■
Electrical test @ room temperature	■	■	■
Electrical test @ low and high temperature		■	■
Screening according to ESCC 9000		□ [2]	■
Lot failure checks according to ESCC 9000			■
Production lot validation testing (LAT) according to ESCC 9000			■
Customer pre-cap inspection possible			□
Data package available for order			□
Dedicated tests available on request			□
Parts selection available for order			□

[1] *For Flight Models the related Procurement Specification is relevant only.*
[2] *Partial screening. Full screening available for order.*

INTENDED USE

The SPLVDS104RH is a radiation hardened high-reliability integrated circuit for space applications. It comes in a hermetic ceramic power flatpack 16. The device can be ordered in 3 quality levels - EM, QM and FM:

Engineering Models (EM) are basically electrically tested. They are for evaluation purposes only and are NOT suitable for qualification of modules or for flight missions. Engineering Models are sold „AS IS“ with no warranty of full datasheet compliance.

Qualification Models (QM) are screened for datasheet compliance. They are intended for qualification of modules but are NOT suitable for flight missions.

Flight Models (FM) are qualification tested and intended for flight missions. They are compliant with the SPLVDS104RH Procurement Specification.

PACKING

- The SPLVDS104RH in flatpack 16 are individually inserted in protective plastic carriers.
- The devices in the carriers are inserted in ESD protective plastic bags which are marked with an ESD warning imprint and labelled with the respective device serial number.
- The devices in the ESD bags are packed and delivered in sealed shipping boxes with antistatic foam.

HANDLING PRECAUTIONS

The SPLVDS104RH is susceptible to damage by inproper handling:

- Although the SPLVDS104RH features ESD protection circuitry, permanent damage may occur on devices subjected to high energy ESD. Do not unbag or handle the device without proper ESD precautions applied.
- Take care when removing the device from the protective plastic carrier to avoid bending of the leads and mechanical damage.

IMPORTANT NOTICE